

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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APPLICANT
Tsukasa OOISHI

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FILING DATE
February 13, 2004

GROUP
2827

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
LP		US 5,946,227	08/31/1999	Naji	
↓		US 2003/0156448 A1	08/2003	Hidaka	
		US 6,608,776 B2	08/2003	Hidaka	
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
LP		JP 10-106255 A	04/24/1998	TOSHIBA CORP.			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
LP		"A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell", Scheuerlein et al., ISSCC Digest of Technical Papers, TA7.2, Feb. 2000, pp. 94-95, 128-129, 409-410.
LP		"Nonvolatile RAM based on Magnetic Tunnel Junction Elements", Durlam et al., ISSCC Digest of Technical Papers, TA7.3, Feb. 2000, pp. 96-97, 130-131, 410-411.

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION

 ATTY. DOCKET NO.
067161-0138

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EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
LP		US 6,608,776 B2	08/19/2003	Hidaka	Counterpart of DE 101 23 332
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						Yes	No
LP		DE 101 23 332	11/28/2002	mitsubishi denki k.k.	Counterpart of USP 6,608,776 B2 & USP 2004/0047196 A1		X
		DE 101 33 646 A1	04/18/2002	mitsubishi denki k.k.	Counterpart of USP 6,778,430 B2		X
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		EP 0 507 451 A2	10/07/1992	MITSUBISHI DENKI KABUSHIKI		x	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
LP		Durlam, M. et al., "Nonvolatile RAM based on magnetic tunnel junction elements" IEEE International Solid-State Circuits Conference, 7 to 9 February 2000, pp. 130-131.
		Scheuerlein, R. et al., "A 10ns read and write nonvolatile memory array using a magnetic tunnel junction and FET switch in each cell" IEEE International Solid-State Circuits Conference, 7 to 9 February 2000, pp. 128-129.

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